

NOV 25 2005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Yeshwanth Narendar et al.

Title: METHOD FOR TREATING SEMICONDUCTOR PROCESSING
COMPONENTS AND COMPONENTS FORMED THEREBY

Application No.: 10/824,329

Filed: 04/14/2004

Examiner: Julio J. Maldonado

Group Art Unit: 2823

Atty. Docket No.: 1075-E4371

COMMISSIONER FOR PATENTS

PO Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- ☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

1. an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described below; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance, such as through (i) an English language abstract, (ii) an English language equivalent application, (iii) reference to discussion in the application, or (iv) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that

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| I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mailer Express Mail, in an envelope addressed to the Commissioner for Patents on <u>11/25/05</u> . | |
| <u>Judy Carey</u> Typed or Printed Name | <u>[Signature]</u> Signature |

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indicates the degree of relevance found by the foreign office.

STATEMENT UNDER 37 C.F.R. § 1.704(d)

If the above-identified application is an original application filed on or after May 29, 2000:

- ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

FEES DUE

This Information Disclosure Statement is being filed:


- ☐ within three months of the filing date of a national application or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
- ☐ before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.
- ☒ during the period specified in § 1.97(c). Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application not more than three months prior to the filing of this Information Disclosure Statement.
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Should any PTO fees be necessary for entry of this Information Disclosure Statement, the undersigned hereby authorizes the Commissioner to charge Deposit Account 50-2469.

Respectfully submitted,

Date 11/25/05


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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Substitute for form 1449/APTO

Sheet 1 of 1
(use as many sheets as necessary)
Complete if Known

Application Number: 10/824,329
Filing Date: 04/14/2004
First Named Inventor: Yeshwanth NARENDAR
Group Art Unit: 2823
Examiner Name: MALDONADO, Julio J.
Attorney Docket Number: 1075-E4371

U.S. PATENT DOCUMENTS

| Examiner Initials * | Cite No. ¹ | U.S. Patent Document Number | Kind Code ² (if known) | Name of Patentee or Applicant of Cited Document | Date of Publication of Cited Document MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|---------------------|-----------------------|-----------------------------|-----------------------------------|---|--|---|
| | A | 6,066,572 | | Lu et al. | 05-23-2000 | |
| | B | 6,277,194 | | Thilderkvist et al. | 08-21-2001 | |
| | C | 2004/4012920 | A1 | Zehavi et al. | 07-08-2004 | |
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FOREIGN PATENT DOCUMENTS

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PUBLICATIONS

| Examiner Initials * | Cite No. ¹ | Title of Publication | Date of Publication of Cited Document MM-DD-YYYY |
|---------------------|-----------------------|--|--|
| | D | Wolf et al., "Silicon Processing for the VLSI Era, vol. 1: Processing Technology; Latice Press, pps. 215-216 | 1986 |
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| Examiner Signature | | Date Considered | |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 10 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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